


13.90 (ETH)  
 11.10 (EXT PWR)  
 10.35 (ALT PWR)

164.90±0.20 (TFT)  
 155.08±0.20 (VA)  
 153.84 (AA)  
 10.05  
 16.69  
 17.21  
 100.00±0.20 (TFT)  
 86.92±0.20 (VA)  
 85.63 (AA)  
 94.00  
 102.00  
 177.00  
 185.00  
 4.00  
 4.00  
 Ø3.18

9.00 (USB)  
 10.05 (RS422)  
 11.75 (PWR/COM)  
 1.90±0.10 (TP)  
 3.50±0.20 (TFT)  
 1.10 (SHIM)  
 1.31 (PCB)  
 10.04 (I2C)  
 21.71

3.35  
 3.72  
 3.72  
 35.75  
 27.01  
 14.26  
 9.81  
 16.00  
 28.24  
 68.95  
 6.78  
 2.45  
 3.44  
 3.13  
 34.91  
 11.69

\*Unmarked Tolerances ±0.50

Dept. D & D	Technical reference	Created by Dan Divino 6/11/2019	Approved by
		Document type Drawing	Document status Released
		Title eGTT70-TPC with Capacitive Touch and all Headers	
Rev. 1.0	Date of issue 6/11/2019	Sheet 1/1	