

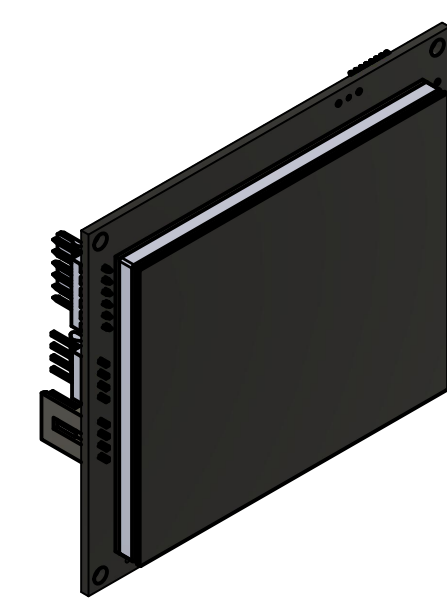
8.80 (USB)  
8.80 (Mass Storage)

10.03 (RS422)


90.00  
76.90 (TFT)  
6.55  
71.08 (VA)  
8.53  
70.08 (AA)  
9.96  
78.00  
63.90 (TFT)  
53.56 (VA)  
52.56 (AA)  
72.00  
84.00  
3.00  
3.00  
Ø3.18

11.75 (PWR/COM)  
10.04 (I2C)  
10.40 (ALT PWR)  
1.90±0.20 (TP)  
3.20±0.10 (TFT)  
1.10 (SHIM)  
1.83 (PCB)  
19.79

2.94  
4.10  
28.69  
9.25  
48.60  
24.35  
36.25  
3.42  
3.44  
44.53  
3.87  
3.00



\*Unmarked Tolerance ±0.50

|   |                           |   |                             |
|---|---------------------------|---|-----------------------------|
| Dept.<br>D&D  | Technical reference       | Created by<br>Dan Divino 6/3/2019                                       | Approved by                 |
|  |                           | Document type<br>Drawing  | Document status<br>Released |
|   |                           | Title<br>eGTT35A-TPC<br>with Capacitive touch<br>And all header options |                             |
| Rev.<br>1.0   | Date of issue<br>6/3/2019 | Sheet<br>1/1  |                             |